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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/676,865	09/30/2003	Seok-Jun Won	9898-305	7630	
20575	7590 12/01/2005		EXAMINER		
	OHNSON & MCCOL	LOKE, STEVEN HO YIN			
210 SW MORRISON STREET, SUITE 400 PORTLAND, OR 97204		`E 400	ART UNIT	PAPER NUMBER	
TORTERNO	, 010 7720		2811		

DATE MAILED: 12/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	S
Office Action Summary		10/676,865	WON, SEOK-JUN	,
		Examiner	Art Unit	
		Steven Loke	2811	
Period fo	The MAILING DATE of this communication a	L	th the correspondence address	
	IORTENED STATUTORY PERIOD FOR REP	IVIQ SET TO EXDIDE 2 M	ONTH(S) OR THIRTY (30) DAYS	.
WHI0 - Exte after - If NO - Failt Any	CHEVER IS LONGER, FROM THE MAILING ensions of time may be available under the provisions of 37 CFR solve (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory perious to reply within the set or extended period for reply will, by staturely received by the Office later than three months after the mained patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNION (1.136(a). In no event, however, may a rick will apply and will expire SIX (6) MON (ate, cause the application to become AE)	CATION. eply be timely filed THS from the mailing date of this communication ANDONED (35 U.S.C. § 133).	
Status				
1)⊠	Responsive to communication(s) filed on <u>07</u>	September 2005.		
2a)⊠	This action is FINAL . 2b) ☐ Th	nis action is non-final.		
3)□	Since this application is in condition for allow			S
	closed in accordance with the practice under	Ex parte Quayle, 1935 C.D	0. 11, 453 O.G. 213.	
Disposit	ion of Claims			
4)🖾	Claim(s) 1-11 and 31 is/are pending in the a	pplication.		
	4a) Of the above claim(s) is/are withdo	rawn from consideration.		
5)⊠	Claim(s) <u>5-11</u> is/are allowed.			
,	Claim(s) <u>1-4 and 31</u> is/are rejected.			
· ·	Claim(s) is/are objected to.	/		
8)	Claim(s) are subject to restriction and	or election requirement.		
Applicat	ion Papers			
,	The specification is objected to by the Exami			
10)	The drawing(s) filed on is/are: a) a			
	Applicant may not request that any objection to the			
11)	Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the			(a).
Priority	under 35 U.S.C. § 119			
-	Acknowledgment is made of a claim for foreig ☐ All b)☐ Some * c)☐ None of:	gn priority under 35 U.S.C. §	§ 119(a)-(d) or (f).	
	1. Certified copies of the priority docume	nts have been received.		
	2. Certified copies of the priority docume			
	3. Copies of the certified copies of the pr		received in this National Stage	
	application from the International Bure	•		
*	See the attached detailed Office action for a li	st of the certified copies not	received.	
Attachme	nt(s)			
1) Noti	ce of References Cited (PTO-892)		Summary (PTO-413)	
	ce of Draftsperson's Patent Drawing Review (PTO-948) rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/0	-: C -:	s)/Mail Date nformal Patent Application (PTO-152)	
	er No(s)/Mail Date	6) Other:		

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1. Claim 31 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

The specification never discloses a total area of the substantially horizontal surfaces greater than a total area of the substantially vertical surfaces as claimed in claim 31.

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1-4 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Nakajima et al.

In regards to claim 1, Nakajima et al. show all the elements of the claimed invention in fig. 1. It is a flat-type capacitor, comprising: a lower interconnection [8a] on a predetermined portion of a semiconductor substrate [1]; a lower electrode (a bottom portion of the capacitor lower electrode) [21] that has a flat shape, the lower electrode electrically coupled to the lower interconnection [8a], the lower interconnection [8a] disposed below the lower electrode [21]; a concave dielectric layer [25b] disposed on the lower electrode [21]; a concave upper electrode [26] disposed on the dielectric layer;

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a length of a bottom part of the concave upper electrode greater than a length of the lower electrode; a first upper interconnection [12] that is electrically coupled to the lower interconnection when the memory transistor is in an on-state; and a second upper interconnection [13] that is coupled to the first upper interconnection.

In regards to claim 2, Nakajima et al. further disclose the lower electrode [21] is positioned between edges of the concave upper electrode [26].

In regards to claim 3, Nakajima et al. further disclose the lower electrode (a top portion of the capacitor lower electrode) [24] and the upper electrode [26] are composed of TiN.

In regards to claim 4, Nakajima et al. further disclose the dielectric layer [25b] is composed of Ta₂O₅.

4. Applicant's arguments filed 9/7/05 have been fully considered but they are not persuasive.

It is urged, in page 5 of the remarks, that Nakajima clearly states that element 21 is a polycrystalline silicon plug and element 24 is a cup-shaped lower electrode. Since element 21 is also electrically connected to the lower electrode 24, it is considered as a part of the lower electrode. Therefore, a length of a bottom part of the concave upper electrode 26 is greater than a length of the lower electrode 21. Nakajima discloses the claimed subject matters as claimed in claims 1-4.

- 5. Claims 5-11 are allowed.
- 6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

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§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Loke whose telephone number is (571) 272-1657. The examiner can normally be reached on 8:00 am to 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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November 25, 2005